

## ABSTRACT OF THE DISCLOSURE

Pixel areas surrounded by scanning signal lines, video signal lines, and counter signal lines are formed on a liquid-crystal-side surface of a transparent

5 substrate, and include thin-film transistors driven by scanning signals from the scanning signal lines, pixel electrodes to which video signals from the video signal lines are supplied via the thin-film transistors, and counter electrodes spaced apart from the pixel electrodes

10 and connected to the counter signal lines. To prevent leakage current from flowing from the scanning signal lines or the video signal lines to the counter signal lines, a common line is formed around a display area constituted by the pixel areas, the video signal lines

15 and the scanning signal lines are connected to the common line by non-linear elements, and the counter signal lines are connected to the common line by high-resistance elements.